



Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<b>Complete if Known</b>	
				Application Number	10/650,363
				Filing Date	August 27, 2003
				First Named Inventor	Harshvardhan Sharangpani
				Art Unit	2171
				Examiner Name	To be assigned
Sheet	2	of	2	Attorney Docket Number	6037P001

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
Rmt	C	T. MATOBA ET AL., "A Rapid Turnaround Design of a High Speed VLSI Search Processor," Integration, the VLSI Journal, February 10, 1991, pp. 319-337,	
↓		Vol. 10, No. 3, North-Holland Publishing Company, Amsterdam, NL.	
↓	D	International Search Report dated November 4, 2004 (6 pages).	

Examiner Signature	<i>Ref'd Mr. Smith</i>	Date Considered	5/18/08
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number. ‡Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/088 (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.  
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450